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<u>LMA</u>	CS L Bit	CS D Bit	Operating Mode
0	X	0	16 Bit Mode
0	Х	1	32 Bit Mode
1	0	0	16 Bit Compatibility Mode
1	0	1	32 Bit Compatibility Mode
1	1	0	32/64 Mode
1	1	1	Reserved

70

Fig. 4

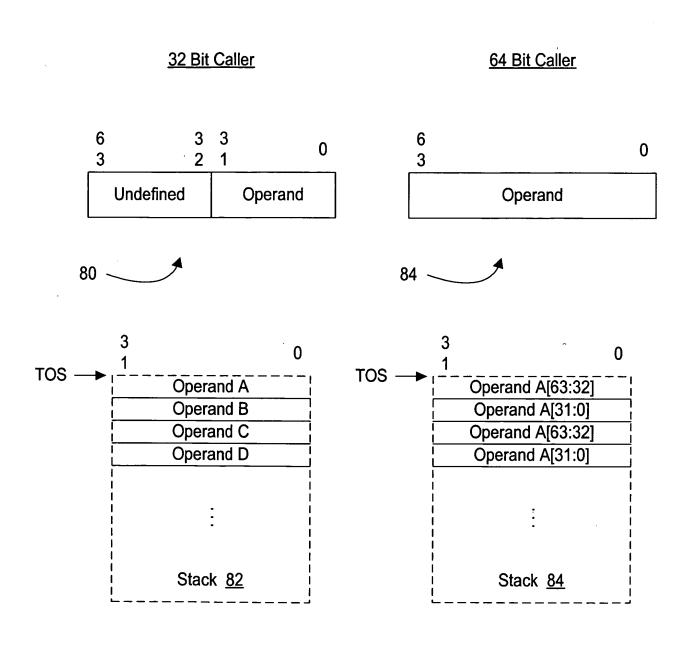
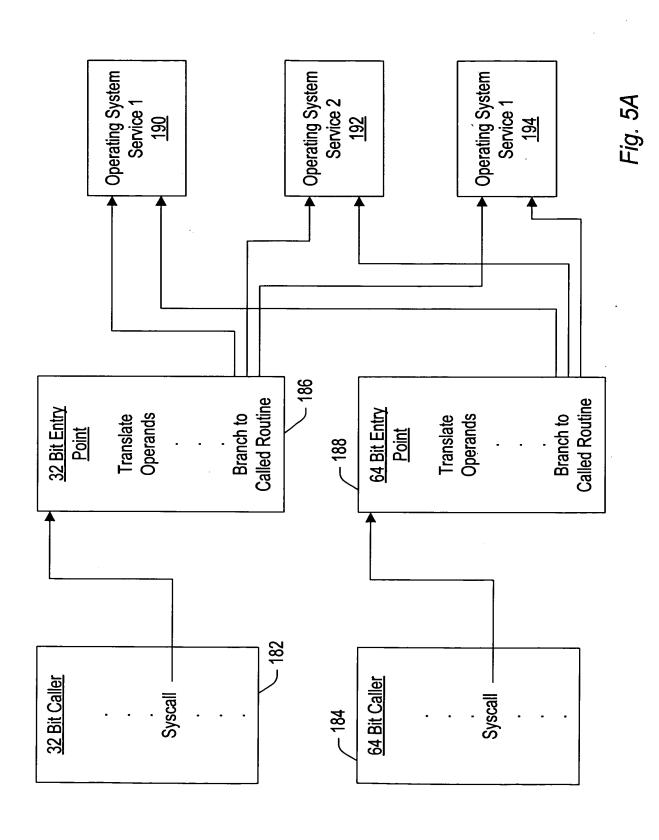


Fig. 5





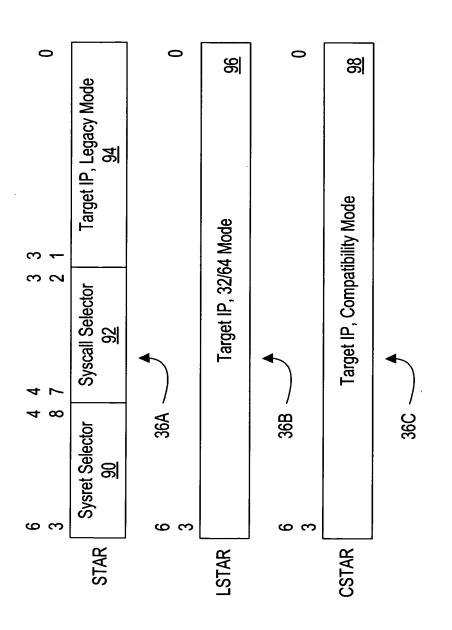
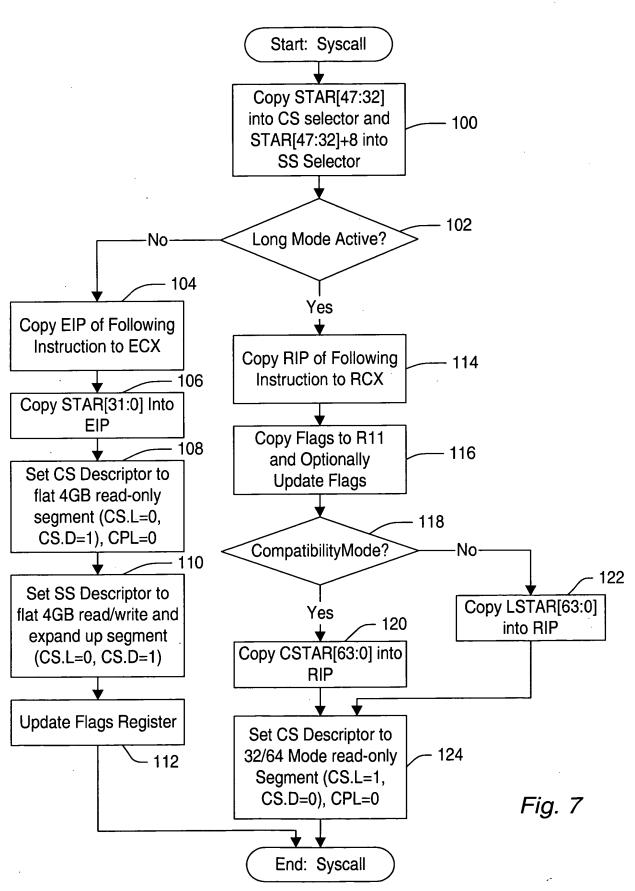
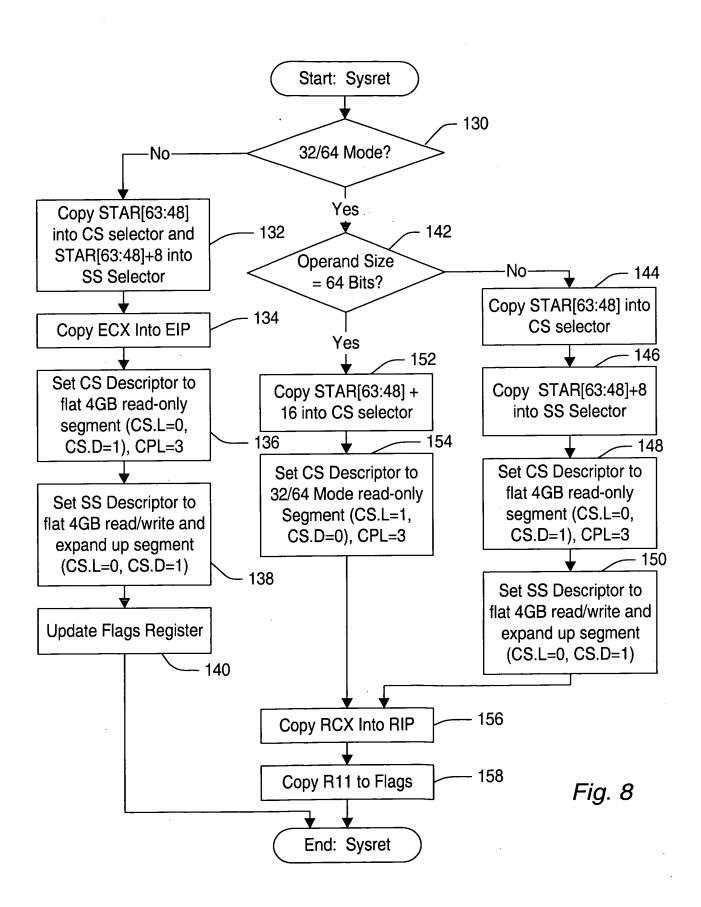
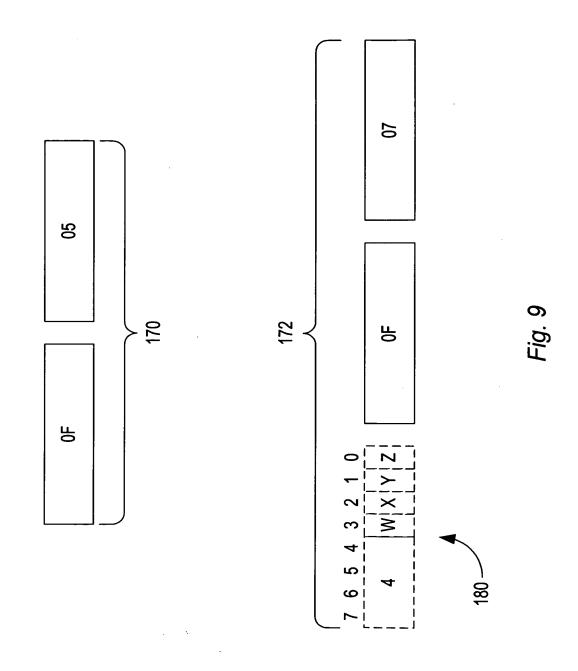


Fig. 6







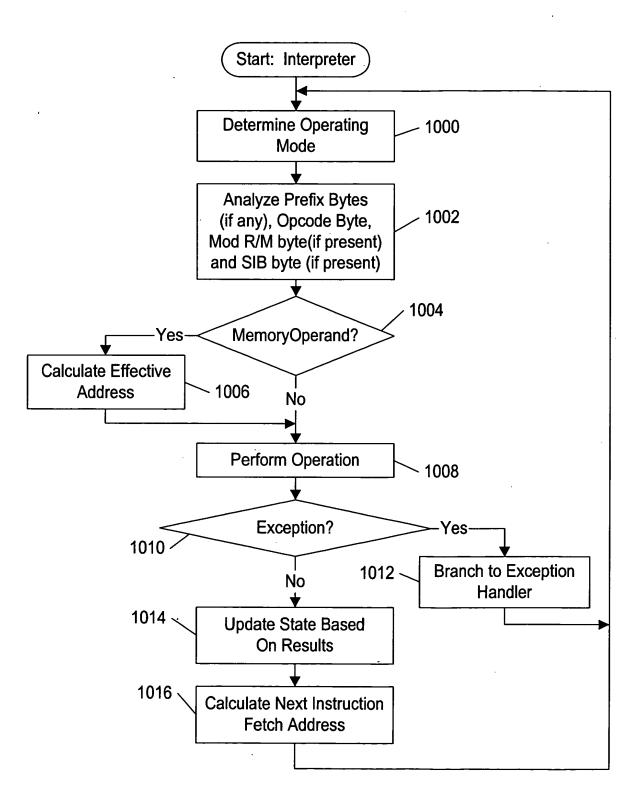


Fig. 10

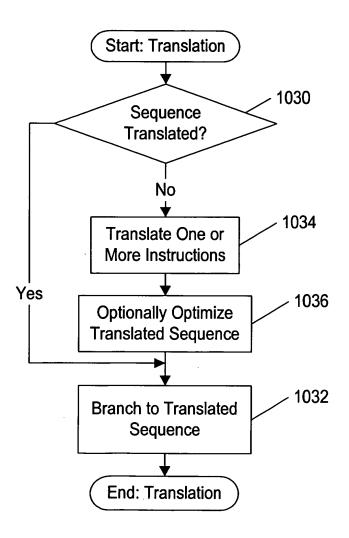


Fig. 11

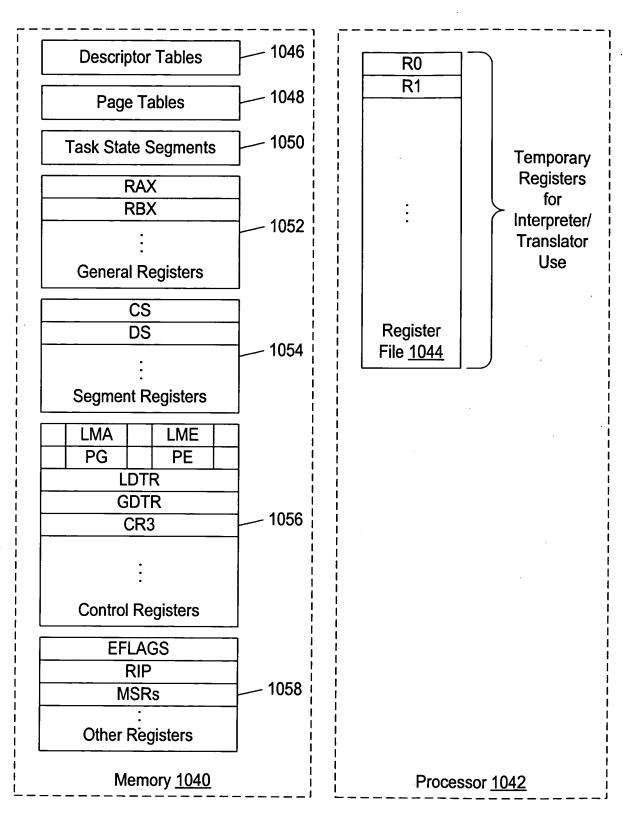


Fig. 12

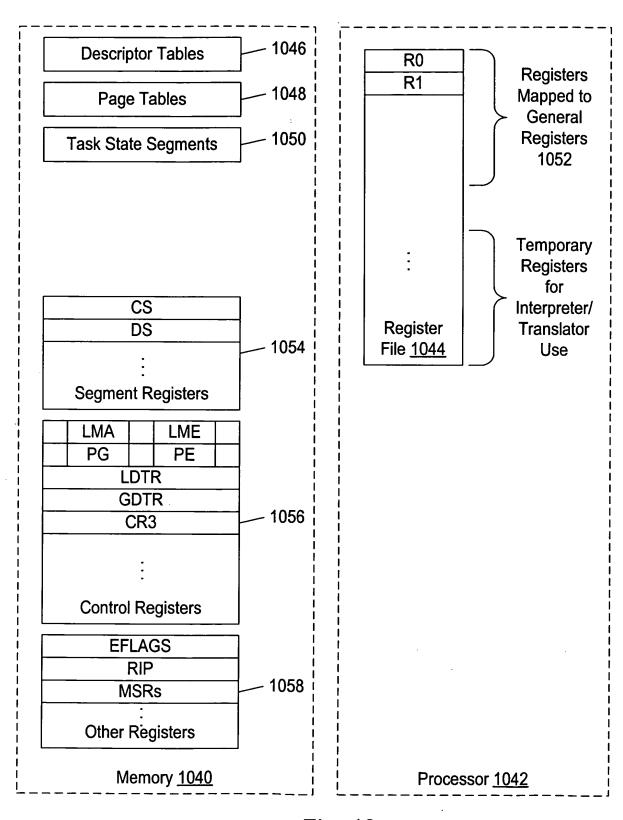


Fig. 13

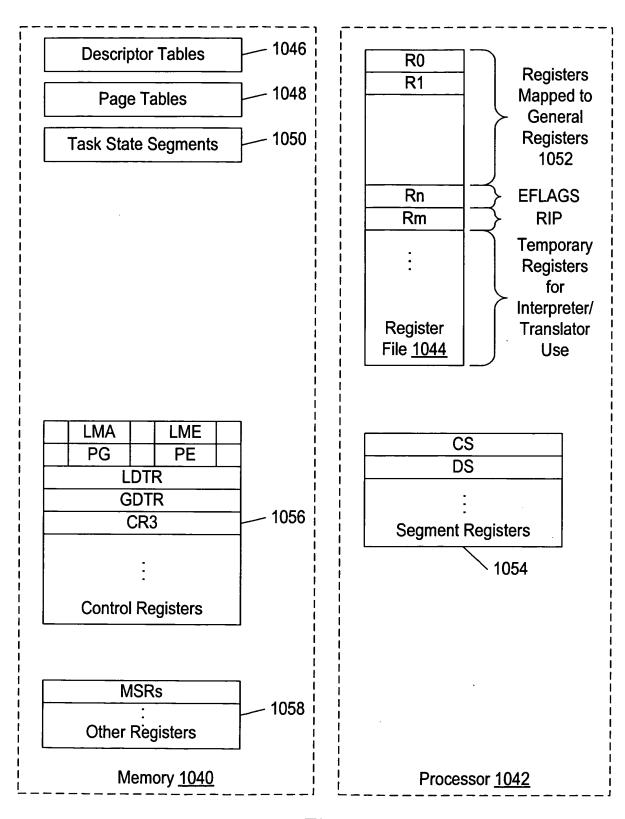


Fig. 14

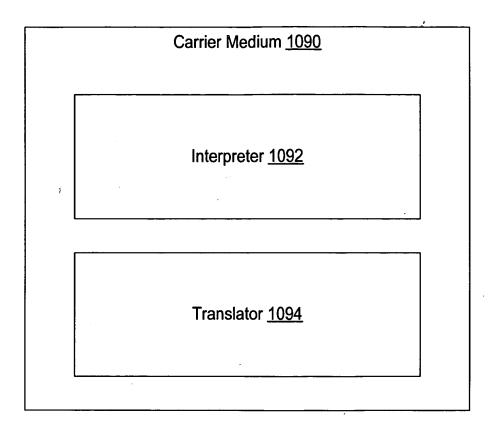
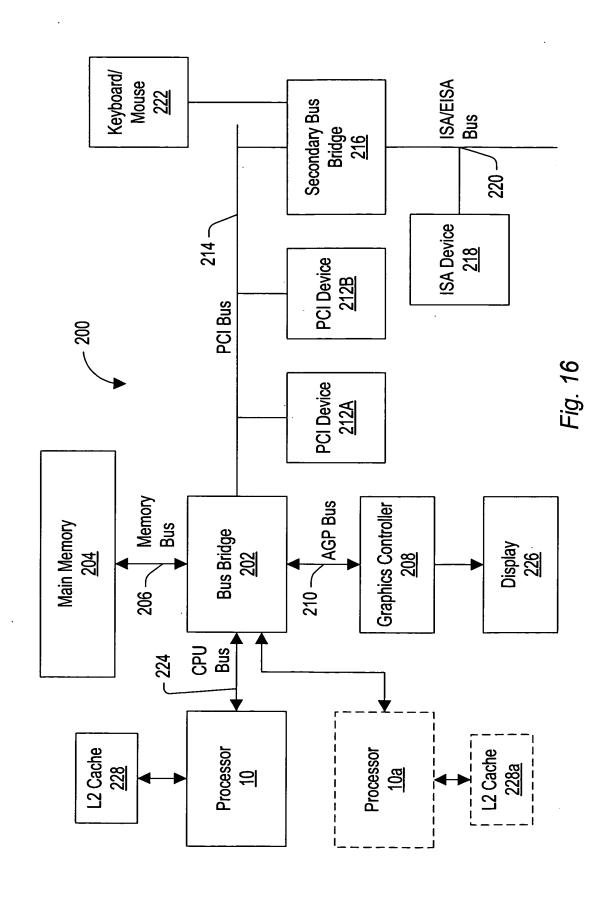


Fig. 15



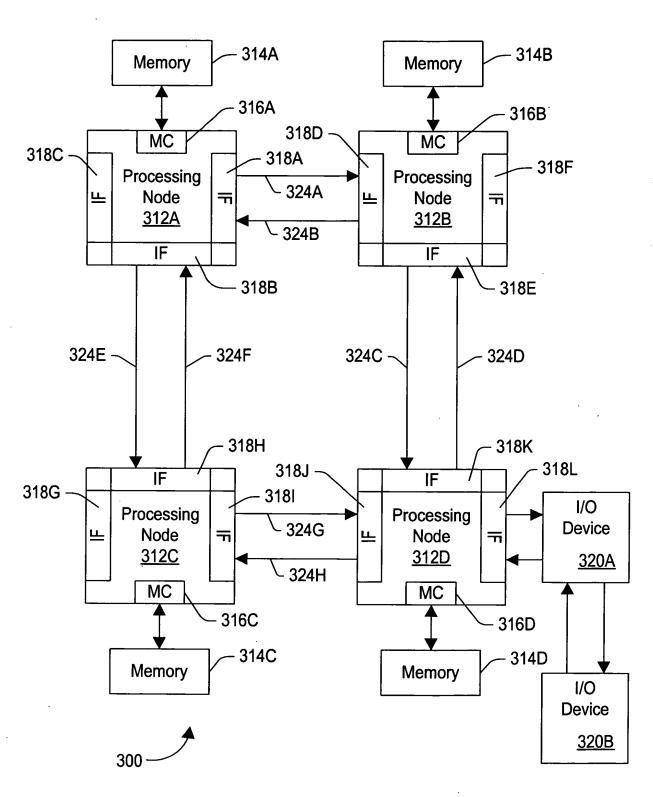


Fig. 17